



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/761,455	01/20/2004	Alessandro Frivoli	PF030008	4058

7590 07/14/2005

JOSEPH S. TRIPOLI, PATENT OPERATIONS
THOMSON LICENSING INC.
P.O. BOX 5312
PRINCETON, NJ 08543-5312

EXAMINER

VU, DAVID HUNG

ART UNIT	PAPER NUMBER
----------	--------------

2828

DATE MAILED: 07/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/761,455

Applicant(s)

FRIVOLI ET AL.

Examiner

David Vu

Art Unit

2828

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-10 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-10 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 1/20/04
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: ____

DETAILED ACTION

Specification

1. Applicant's cooperation is requested in correcting any errors of which applicant may become aware in the specification.

Claim Objections

2. Claim 6 is objected to because of the following informalities: "with by means" is awkward. Appropriate correction is required.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1 and 6 are rejected under 35 U.S.C. 102(e) as being anticipated by Libera, U.S. Pat No 6,654,224.

Libera discloses the claimed invention including PTC resistors 50,60; degaussing coils 70-80; means 30-40 for selecting an impedance of the degaussing circuit based on

the voltage fed to the degaussing circuit; and switch in the driver circuit (abstract, figures 1-5, column 5, lines 1-31, column 7, lines 24-54, column 8, lines 1-39).

5. Claims 1-3, 5-8, and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Yutaka of record, JP Pat No 10-308947.

Yutaka discloses the claimed invention including PTC resistors 4,7; degaussing coil 5; and means 13 for selecting an impedance of the degaussing circuit based on the voltage fed to the degaussing circuit (abstract, figure 1).

Regarding claims 2,7, figure 1 shows voltage sensor circuit 12 for measuring the voltage and switch 6 is controlled by the voltage sensor circuit.

Regarding claims 3,8, switch 6 selectively connects a first or second PTC 4,7 in the degaussing circuit.

Regarding claims 5,10, the claimed method is inherent in the Yutaka reference.

6. Claims 1-3, 5-8, and 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Yuuji of record, JP Pat No 01097091.

Yuuji discloses the claimed invention including PTC resistors 9-10; degaussing coil 14; and means 31-32, 34-35 for selecting an impedance of the degaussing circuit based on the voltage fed to the degaussing circuit (abstract, figure 1, page 5).

Regarding claims 2,7, figure 1 shows voltage sensor circuit A-B for measuring the voltage and switches 19,23 controlled by the voltage sensor circuit.

Regarding claims 3,8, switches 19,23 selectively connect a first or second PTC in the degaussing circuit.

Regarding claims 5,10, the claimed method is inherent in the Yuuji reference.

Claim Rejections - 35 USC § 103

7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 2-5, 7-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Libera in view of Yutaka.

Libera as discussed from the above, essentially discloses the claimed invention but fails to explicitly disclose a voltage sensor circuit. Yutaka discloses a degaussing circuit with a voltage sensor circuit 12 in combination with a relay control circuit for controlling a switch (figure 1). It would have been obvious to one having ordinary skill in the art at the time of applicant's claimed invention was made to have provided the Libera reference with the voltage sensor circuit; thus, voltage supplied to the degaussing circuit would have been detected. Note that the Libera reference does disclose a driver circuit including a switching transistor (column 5, lines 23-32).

Regarding claims 3,8, the switch in the driver circuit selectively connects a first or second PTC in the degaussing circuit.

Regarding claims 4,9, the Libera reference discloses a degaussing circuit with degaussing coils selectively connected in series or parallel (figures 3-5).

Regarding claims 5,10, the claimed method is inherent in the Yutaka and Libera combination.

9. Claims 4 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yutaka in view of Libera.

Yutaka as discussed from the above, essentially discloses the claimed invention but fails to explicitly disclose the two degaussing coils are selectively connected in series or parallel. Libera discloses a degaussing circuit with degaussing coils 340-350, 550,570) either connected in series or parallel (figures 3-5). It would have been obvious to one having ordinary skill in the art at the time of applicant's claimed invention was made to have connected the degaussing coils in series or parallel depending on the power source; thus, current flowing through each coil would have been balanced out.

10. Claims 4 and 9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Yuuji in view of Libera.

Yuuji as discussed from the above, essentially discloses the claimed invention but fails to explicitly disclose the two degaussing coils are selectively connected in series or parallel. Libera discloses a degaussing circuit with degaussing coils 340-350, 550,570) either connected in series or parallel (figures 3-5). It would have been obvious to one having ordinary skill in the art at the time of applicant's claimed invention was made to have connected the degaussing coils in series or parallel depending on the power source; thus, current flowing through each coil would have been balanced out.

Art Unit: 2828

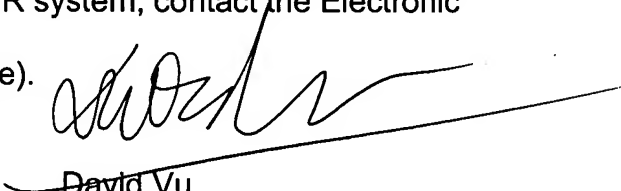
Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David Vu whose telephone number is (571) 272-1831.

The examiner can normally be reached on M-F 8am-430pm.

The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



David Vu
Primary Examiner
Art Unit 2828

dv